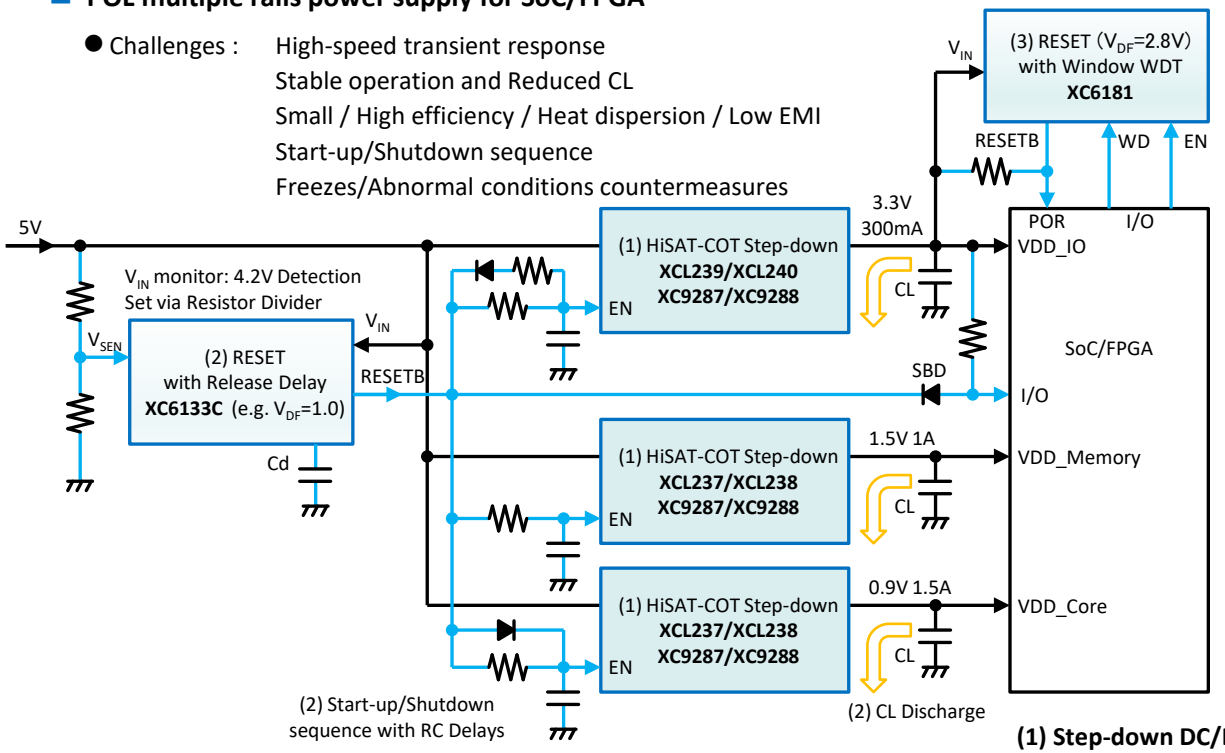
5V : POL Solutions for Multiple Power Rails

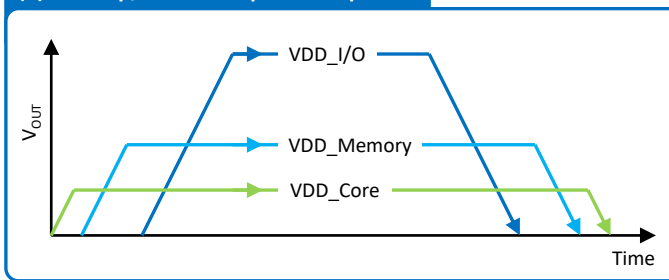
POL multiple rails power supply for SoC/FPGA

● Challenges : High-speed transient response
Stable operation and Reduced CL
Small / High efficiency / Heat dispersion / Low EMI
Start-up/Shutdown sequence
Freezes/Abnormal conditions countermeasures

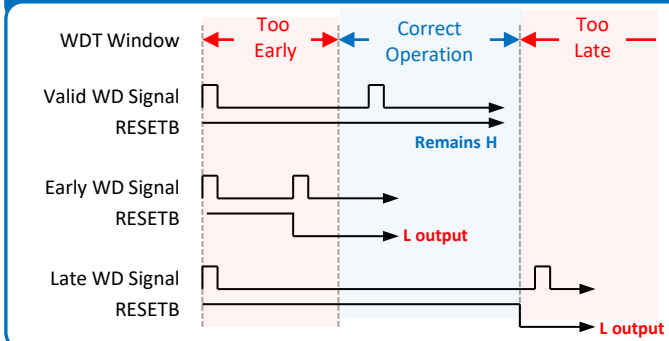


Block	Product	Features
(1) Step-down DC/DC	**XCL239 / XCL240** **XCL237 / XCL238** FEATURED	3MHz, Built-in inductor, HiSAT-COT, F-PWM, PWM/PFM 1A (XCL239/XCL240), 1.5A (XCL237/XCL238)
	XC9287 / XC9288 FEATURED	HiSAT-COT 1.2MHz/3MHz, 1.5A, F-PWM, PWM/PFM
(2) RESET IC	**XC6133**	Separated sense pin, High accuracy, 125℃ Externally adj. detect/release delay
(3) REST IC with Window WDT	**XC6185 / XC6186** **XC6187 / XC6188** UNDER DEVELOPMENT	Window watchdog timer Iq=3μA, WDT/Built-in detect delay **Others: XC6181/XC6182/XC6183/XC6184 (Normal WDT)** UNDER DEVELOPMENT

(2) Start-up/Shutdown power sequence



(3) Countermeasures for Freezes and Abnormal Conditions Using a Window Watchdog Timer



(1) Step-down DC/DC for FPGA/SoC (POL converter)

High-speed transient response HiSAT-COT controlled DC/DC arranged as **POL.**
Built-in inductor Micro DC/DC realizes miniaturization and low EMI.
(XCL239/XCL240, XCL237/XCL238)

(2)(3) 5V Input Monitoring and Start-up/Shutdown power supply sequence

Monitor using the XC6133 voltage detector, suitable for industrial machinery with high precision and high-temperature operation capability. Sequence is achieved by driving the EN of each DC/DC converter using an RC delay.

(3) Freeze countermeasure with Window watchdog timer

Monitoring of 3.3V line voltage and detection of freezing by the WD function. The WD signal from the SoC/FPGA is judged as abnormal if it is either too early or too late, resulting in a reset.

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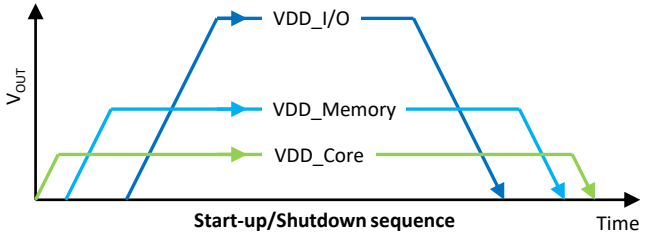
Start-up/Shutdown Sequence Control of Multiple Power Rails

For multiple power rails of SoC/FPGA, Start-up/Shutdown Sequence Control

Technical trend and challenges

- Start-up/shutdown sequences are required for multiple power rails for such as SoC/FPGA.
- As well as starting up using PG of each DC/DC, it is also important to shut down safely under control.

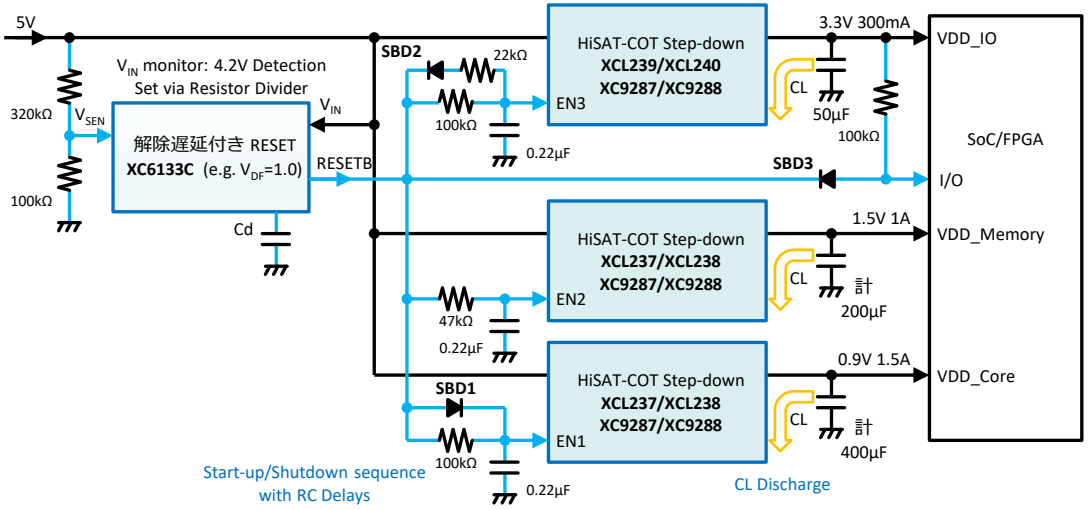


TOREX Proposal : Start-up/shutdown sequence using EN voltage and CL discharge of DC/DC converter

- By applying an RC delay to the EN pin, both start-up and shutdown sequencing can be realized. (Conventional Power Good pin methods typically support start-up sequencing only.)

The rise of each voltage line is via soft start, and the fall is via CL discharge. The timing is controlled by the RC delay at the EN pin of each DC/DC converter.

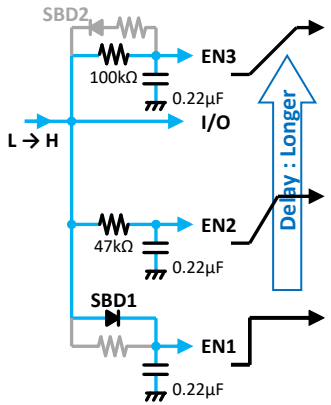
➢ **Circuit Example Using a Reset IC, RC Delays, and DC/DC Converters**



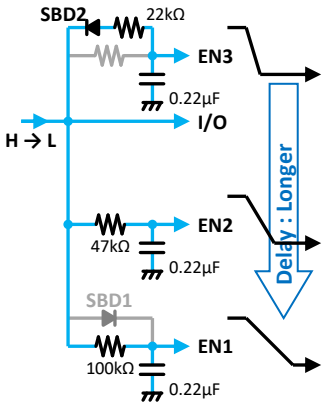
- A RESET IC monitoring the 5V rail drives the RC-delay networks connected to each EN pin. The ON/OFF timing of each EN pin is controlled by the corresponding RC delay.
- **Start-up** : **SBD1** bypasses the RC delay on **EN1**, asserting **EN1** and bringing up **VDD_Core**. Subsequently, **EN2 and EN3** turn ON in order according to their RC delays, powering **VDD_Memory and VDD_IO**.
- **Shutdown** : Through **SBD3**, **the SoC/FPGA I/O** is notified, and the SoC/FPGA executes a shutdown routine. Then **EN3** is driven via **SBD2** through its RC network to turn **VDD_IO** OFF. Following the RC delays, **EN2 and EN1** turn OFF in sequence, shutting down **VDD_Memory and VDD_Core**.

EN Pin ON/OFF Sequencing Control via RC Delays

Startup: RC & EN1–EN3



Shutdown: RC & EN1–EN3



The RC values are switched by the operation of SBD1 and SBD2 during start-up and shutdown.
(**Greyed out** indicates components disabled by SBD operation)

Appendix : The delay time until each DC/DC turns ON/OFF is

$$t_{delay(ON)} = R \times C \times (-\ln((V_{IN} - V_{EN}) / V_{IN}))$$

$$t_{delay(OFF)} = R \times C \times (-\ln(V_{EN} / V_{IN}))$$

V_{EN} : Threshold voltage of EN

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HiSAT-COT® Control for Fast Transient Response

TOREX original COT control : HiSAT-COT®

● Technical trend and challenges

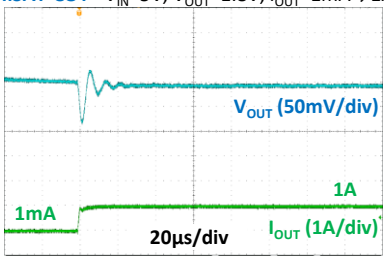
- Stable power supply including transient response to MCU/SoC/FPGA, etc.
- Miniaturization of circuits including peripheral components, and low EMI.

● TOREX Proposal : HiSAT-COT® controlled Step-down DC/DC converter

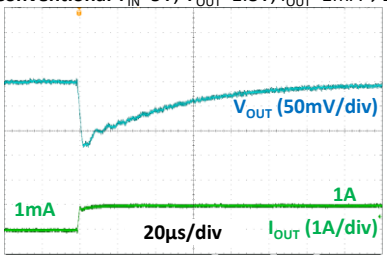
➢ Significantly faster transient response

- Compared to conventional PWM and PWM/PFM control, it achieves **overwhelmingly fast response** and thus **good voltage stability**.

HiSAT-COT® V_{IN}=5V, V_{OUT}=1.8V, I_{OUT}=1mA→1A



Conventional V_{IN}=5V, V_{OUT}=1.8V, I_{OUT}=1mA→1A

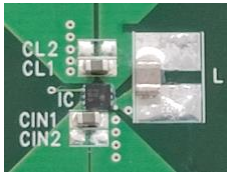


➢ Miniaturization including peripheral components

- High-speed transient response enables **significant reduction of large capacitance** required due to lack of response of conventional PWM.
- Unlike conventional PWM phase compensation, load capacitance CL can be reduced. Also **supports a significant reduction in effective capacitance due to the bias effect of ultra-small Ceramic capacitors.**

HiSAT-COT®

Conventional



➢ Ideal for FPGA/SoC POL power supplies due to its compact size and high-speed response

Overview of COT control and HiSAT-COT®

What is COT (Constant on time) control?

- PFM control with the "ton" determined by V_{IN} and V_{OUT} voltages, resulting that appears to be PWM control with constant frequency (fosc).
  High-speed PFM comparator enables fast transient response.
- Generate **"ton"** in CCM of the targeted fosc from the V_{IN} and V_{OUT} set voltages so that it appears to be a constant frequency PWM control.

● CCM (Continuous Conduction Mode) operation

- Ideal Duty ratio and ton of step-down DC/DC at CCM PWM operation are
  ton = (1/fosc) x Duty = (1/fosc) x (V_{OUT} / V_{IN}) .
  If there is no loss, **Duty ratio is constant** even if I_{OUT} rises.

● How to determine the oscillation frequency of COT control

- Generate the ton of COT control to be the ton of ideal PWM control.
- Continuous mode operation with this ton operates with the same duty as PWM control at the oscillation frequency fosc.

● COT issues and HiSAT-COT®

HiSAT-COT improves the issues of COT control with its own circuits.

- Improved issue of increased oscillation frequency due to output current.
- Improved the deterioration of load stability with an original circuit with an additional amplifier.

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TOREX Built-In Inductor Micro DC/DC for Achieving Small / Low EMI

TOREX original Built-in inductor Micro DC/DC XCL Series

Technical trend and challenges

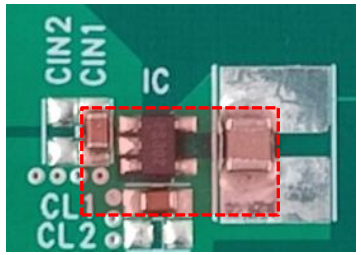
- For stable operation of devices, it is important to place power supply ICs close to MCUs and FPGAs. Especially in cases where multiple power supplies are required, selecting power supply ICs suitable for POL (Point of Load) is a challenge.
- Miniaturization of power circuits including ICs and low EMI are essential.

TOREX Proposal : Built-in inductor Micro DC/DC

➢**Significant miniaturization of power supply circuit**

- Achieves a significant reduction in mounting area and providing smallest class of power supply solution.
- Unique package structure / Optimum inductor for the internal IC.
- Efficient heat dissipation performance with structures that connect IC/coil and substrate with low thermal resistance.

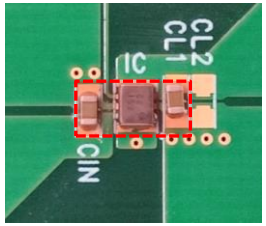
9.6×5.8mm = 56mm²



Stand-alone DC/DC
External parts : 3 pcs

65% reduction

6.5×3.0mm = 19.5mm²



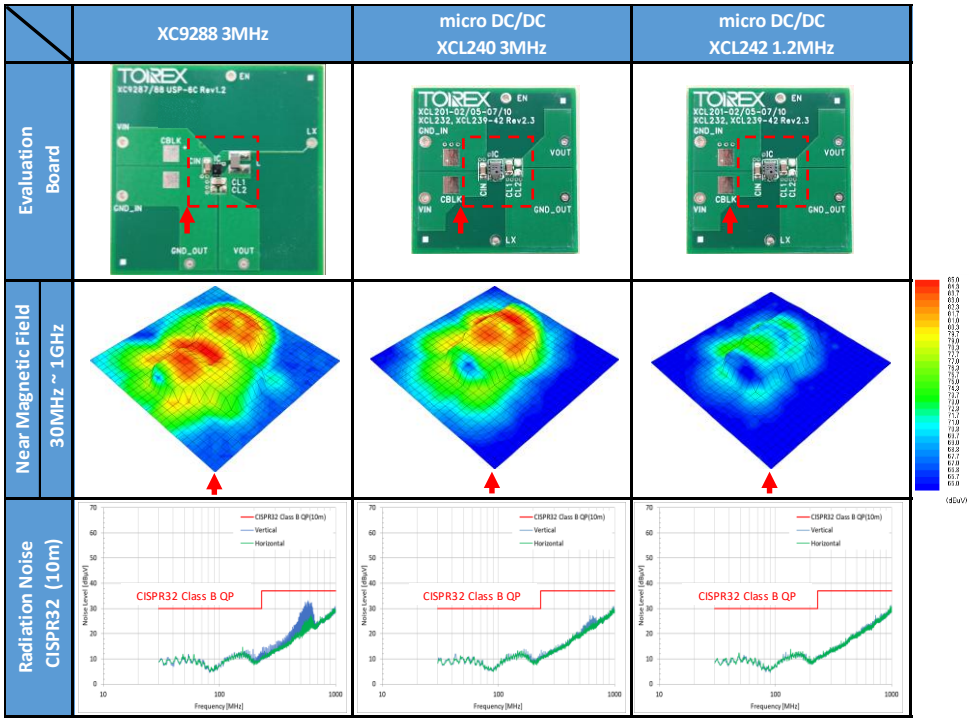
Built-in inductor Micro DC/DC
External parts : 2 pcs

➢**EMI reduction due to unique Built-in inductor structure**

✓The pocket-type structure covering the IC with a coil and the optimum placement of the IC enable a **significant reduction of radiated noise** compared to the stand-alone IC.

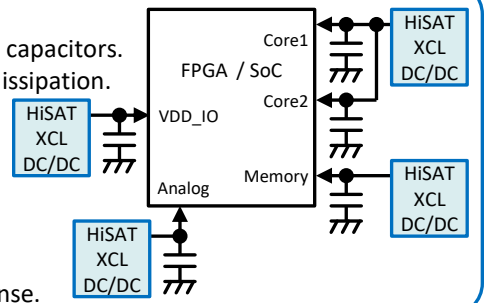
✓Can be placed near RF ICs/Sensors, etc., contributing to miniaturization.

EMI comparison of Built-in inductor Micro DC/DC and stand-alone DC/DC



Benefits of POL (Point of Load) power supply and Micro DC/DC & HiSAT-COT®

- Shorter power supply wiring length.
  In addition to stable operation, reduced capacitors.
  Heat source dispersion facilitates heat dissipation.
- Using **Micro DC/DC XCL Series** with built-in inductor for **POL converter** enables further miniaturization, lower EMI, and easier design.
- **HiSAT-COT** provides highly stable power supply, including transient response.



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